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<b>Track Co-Chairs:</b>	<i>Mercedes G. Merayo, University Complutense of Madrid, Spain</i>
	<i>Gwen Salaün, Grenoble INP, France</i>
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<i>Pavel Jančík, Charles University in Prague, Czech Republic</i>	
<i>Jan Kofroň, Charles University in Prague, Czech Republic</i>	
<b>Predictive Runtime Enforcement</b>	1628
<i>Srinivas Pinisetty, Aalto University, Finland</i>	
<i>Viorel Preoteasa, Aalto University, Finland</i>	
<i>Stavros Tripakis, Aalto University, University of California, Berkeley, Finland</i>	
<i>Thierry Jéron, INRIA Rennes - Bretagne Atlantique, France</i>	
<i>Yliès Falcone, Université Grenoble Alpes, France</i>	
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<i>Said Daoudagh, CNR-ISTI, Italy</i>	
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<i>Isabela Silva, Federal University of Amazonas, Brazil</i>	
<i>Celso Carvalho, Federal University of Amazonas, Brazil</i>	
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<i>Ricardo Ferreira, Federal University of Viçosa, Brazil</i>	
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<i>Maicon Bernardino, Pontifical Catholic University of Rio Grande do Sul, Brazil</i>	
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<i>Luciano Baresi, Politecnico di Milano, Italy</i>	
<i>Mohammad Mehdi Pourhashem Kallehbasti, Politecnico di Milano, Italy</i>	
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<i>Frédéric Mesnard, Université de La Réunion, France</i>	
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<i>Hüsnü Yenigün, Sabancı University, Turkey</i>	
<i>Nina Yevtushenko, Tomsk State University, Russia</i>	
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<i>Carlos Olarte, Universidade Federal do Rio Grande do Norte, Brazil</i>	
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<i>Graeme Smith, The University of Queensland, Australia</i>	
<i>John Derrick, University of Sheffield, UK</i>	
<b>Formal Modeling and Analysis of RAMP Transaction Systems .....</b>	1700
<i>Si Liu, University of Illinois Urbana-Champaign, USA</i>	
<i>Peter Csaba Ölveczky, University of Oslo, Norway</i>	
<i>Muntasir Raihan Rahman, University of Illinois Urbana-Champaign, USA</i>	
<i>Jatin Ganhatra, University of Illinois Urbana-Champaign, USA</i>	
<i>Indranil Gupta, University of Illinois Urbana-Champaign, USA</i>	
<i>José Meseguer, University of Illinois Urbana-Champaign, USA</i>	
<b>Student Research Competition Paper</b>	
<b>Student Research Abstract: Towards a Combinatorial Approach for Undiscounted MDPs ....</b>	1708
<i>Vahid Hashemi, Max Planck Institute for Informatics, Germany</i>	

## **Embedded Systems Track**

**Track Co-Chairs:** *Marco Di Natale, Scuola Superiore S. Anna, Italy  
Li-Pin Chang, National Chiao-Tung University, Taiwan*

<b>Balanced Loop Retiming to Effectively Architect STT-RAM-Based Hybrid Cache for VLIW Processors .....</b>	1710
<i>Keni Qiu, Capital Normal University, China Weigong Zhang, Capital Normal University, China Xiaoqiang Wu, Guangdong Electronics Industry Institute, China Xiaoyan Zhu, Capital Normal University, China Jing Wang, Capital Normal University, China Yuanchao Xu, Capital Normal University, China Chun Jason Xue, City University of Hong Kong, Hong Kong</i>	
<b>Co-Cap: Energy-Efficient Cooperative CPU-GPU Frequency Capping for Mobile Games .....</b>	1717
<i>Jurn-Gyu Park, University of California, Irvine, USA Chen-Ying Hsieh, University of California, Irvine, USA Nikil Dutt, University of California, Irvine, USA Sung-Soo Lim, Kookmin University, South Korea</i>	
<b>ARTE: Arduino Real-Time Extension for Programming Multitasking Applications .....</b>	1724
<i>Pasquale Buonocunto, Scuola Superiore Sant'Anna, Italy Alessandro Biondi, Scuola Superiore Sant'Anna, Italy Marco Pagani, Scuola Superiore Sant'Anna, Italy Mauro Marinoni, Scuola Superiore Sant'Anna, Italy Giorgio Buttazzo, Scuola Superiore Sant'Anna, Italy</i>	
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<i>Ehsan Atoofian, Lakehead University, Canada</i>	
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<i>Shounak Chakraborty, Indian Institute of Technology Guwahati, India Shirshendu Das, Indian Institute of Technology Guwahati, India Hemangee K. Kapoor, Indian Institute of Technology Guwahati, India</i>	
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<i>Shirshendu Das, Indian Institute of Information Technology Guwahati, India Hemangee K. Kapoor, Indian Institute of Technology Guwahati, India</i>	
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<i>Luca Abeni, University of Trento, Italy Daniele Fontanelli, University of Trento, Italy Luigi Palopoli, University of Trento, Italy Bernardo Villalba Frias, University of Trento, Italy</i>	
<b>FIDE – An FMI Integrated Development Environment .....</b>	1759
<i>Fabio Cremona, University of California, Berkeley, and Scuola Superiore Sant'Anna, USA Marten Lohstroh, University of California, Berkeley, USA Stavros Tripakis, University of California, Berkeley, and Aalto University, USA Christopher Brooks, University of California, Berkeley, USA Edward A. Lee, University of California, Berkeley, USA</i>	

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<i>Chin-Fu Kuo, National University of Kaohsiung, Taiwan</i>	
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<i>Yung-Feng Lu, National Taichung University of Science and Technology, Taiwan</i>	
<i>Shih-Chun Chou, Institute for Information Industry, Taiwan</i>	
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<i>Sophal Minh, University of Seoul, South Korea</i>	
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<i>Jen-Wei Hsieh, National Taiwan University of Science and Technology, Taiwan</i>	
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<b>Safety Analysis Generation from Prototyping Models for Transportation Systems .....</b>	1777
<i>Jean Godot, FAAR Industry, France</i>	
<i>Sebastien Saudrais, ESTACA'Lab, France</i>	
<i>Adil Alif, FAAR Industry, France</i>	
<i>Bertrand Barbedette, ESTACA'Lab, France</i>	
<i>Cherif Larouci, ESTACA'Lab, France</i>	
<b>Software for Embedded Systems: A Quality Assessment based on Improved ODC Taxonomy ...</b>	1780
<i>Nuno Silva, University of Coimbra, Portugal</i>	
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<b>Object Oriented Programming Languages and Systems Track</b>	
<i>Track Co-Chairs: Davide Ancona, Universita` di Genova, Italy</i>	
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<i>Eli Sennesh, Technion, Israel</i>	
<i>Yossi Gil, Technion, Israel</i>	
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<i>Peter Mills, University of Idaho, USA</i>	
<i>Clinton Jeffery, University of Idaho, USA</i>	
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<i>Tobias Pape, University of Potsdam, Germany</i>	
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<i>Robert Hirschfeld, University of Potsdam, Germany</i>	
<b>From Atomic Variables to Data-Centric Concurrency Control .....</b>	1806
<i>Hervé Paulino, NOVA University of Lisbon, Portugal</i>	
<i>Daniel Parreira, NOVA University of Lisbon, Portugal</i>	
<i>Nuno Delgado, NOVA University of Lisbon, Portugal</i>	
<i>António Ravara, NOVA University of Lisbon, Portugal</i>	
<i>Ana Matos, University of Lisbon, Portugal</i>	

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<b>On the Criteria for Prioritizing Code Anomalies to Identify Architectural Problems .....</b>	1812
<i>Santiago Vidal, ISISTAN-CONICET, Argentina</i>	
<i>Everton Guimaraes, UNIFOR, Brazil</i>	
<i>Willian Oizumi, Pontifical Catholic University of Rio de Janeiro, Brazil</i>	
<i>Alessandro Garcia, Pontifical Catholic University of Rio de Janeiro, Brazil</i>	
<i>Andrés Díaz Pace, ISISTAN-CONICET, Argentina</i>	
<i>Claudia Marcos, ISISTAN-CIC, Argentina</i>	

<b>Using Field Access Frequency to Optimize Layout of Objects in the JVM .....</b>	1815
<i>Taees Eimouri, University of New Brunswick, Canada</i>	
<i>Kenneth B. Kent, University of New Brunswick, Canada</i>	
<i>Aleksandar Micic, IBM, Canada</i>	
<i>Karl Taylor, IBM, Canada</i>	

<b>Initialize-and-Catch .....</b>	1819
<i>Paola Giannini, Università del Piemonte, Italy</i>	
<i>Marco Servetto, Victoria University of Wellington, New Zealand</i>	
<i>Elena Zucca, Universita' di Genova, Italy</i>	

## **Operating Systems Track**

<b>Track Co-Chairs:</b>	<i>Bongjae Kim, Korea Electronics Technology Institute, South Korea</i>
	<i>George Hamer, South Dakota State University, United States</i>

<b>Track Editorial .....</b>	1823
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<b>i-RAID: A Novel Redundant Storage Architecture for Improving Reliability, Performance, and Life-Span of Solid-State Disk Systems .....</b>	1824
<i>Mingyang Wang, University of Cincinnati, USA</i>	
<i>Yiming Hu, University of Cincinnati, USA</i>	

<b>mmapcopy: Efficient Memory Footprint Reduction using Application Knowledge .....</b>	1832
<i>Ingo Korb, Technical University of Dortmund, Germany</i>	
<i>Helena Kotthaus, Technical University of Dortmund, Germany</i>	
<i>Peter Marwedel, Technical University of Dortmund, Germany</i>	

<b>OS-Level Virtualization for Industrial Automation Systems: Are We There Yet? .....</b>	1838
<i>Alexandru Moga, ABB Corporate Research, Switzerland</i>	
<i>Thanikesavan Sivanthi, ABB Corporate Research, Switzerland</i>	
<i>Carsten Franke, ABB Corporate Research, Switzerland</i>	

<b>Load-Aware Scheduling for Heterogeneous Multi-Core Systems .....</b>	1844
<i>Mohannad Nabelsee, Technische Universität Berlin, Germany</i>	
<i>Anselm Busse, Technische Universität Berlin, Germany</i>	
<i>Helge Parzy jegla, Rostock University, Germany</i>	
<i>Gero Mühl, Rostock University, Germany</i>	

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<i>Jungkil Kim, Samsung Electronics Co., Ltd., South Korea</i>	
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<i>Kwanghyun La, Samsung Electronics Co., Ltd., South Korea</i>	
<i>Wooseok Chang, Samsung Electronics Co., Ltd., South Korea</i>	

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<i>Satoru Takekoshi, University of Tsukuba, Japan</i>	
<i>Takahiro Shinagawa, The University of Tokyo, Japan</i>	
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<i>Yong-Yeon Jo, Hanyang University, South Korea</i>	
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<i>Sang-Wook Kim, Hanyang University, South Korea</i>	
<i>Hyunok Oh, Hanyang University, South Korea</i>	
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<i>Seungjae Baek, Dankook University, South Korea</i>	
<i>Jongmoo Choi, Dankook University, South Korea</i>	
<i>Seongje Cho, Dankook University, South Korea</i>	
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<i>Giuseppe Lipari, University of Lille 1, France</i>	
<i>Andrea Parri, Scuola Superiore Sant'Anna, Italy</i>	
<i>Youcheng Sun, Scuola Superiore Sant'Anna, Italy</i>	
<b>Plate: Persistent Memory Management for Nonvolatile Main Memory .....</b>	1885
<i>Toshihiro Yamauchi, Okayama University, Japan</i>	
<i>Yuta Yamamoto, Okayama University, Japan</i>	
<i>Kengo Nagai, Okayama University, Japan</i>	
<i>Tsukasa Matono, Kyushu University, Japan</i>	
<i>Shinji Inamoto, Kyusyu University, Japan</i>	
<i>Masaya Ichikawa, Kyushu University, Japan</i>	
<i>Masataka Goto, Kyushu University, Japan</i>	
<i>Hideo Taniguchi, Okayama University, Japan</i>	
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<i>Sooyong Kang, Hanyang University, South Korea</i>	
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<i>Taegyu Hwang, Soongsil University, South Korea</i>	
<i>Kisu Kim, Soongsil University, South Korea</i>	
<i>Jeonghwan Lee, Soongsil University, South Korea</i>	
<i>Jiman Hong, Soongsil University, South Korea</i>	
<i>Dongwan Shin, New Mexico Tech, USA</i>	

<b>NKE – An Embedded Nanokernel for Educational Purpose .....</b>	1900
<i>Celso M. da Costa, State University of Rio Grande do Sul, Brazil</i>	
<i>Cassio Brasil, State University of Rio Grande do Sul, Brazil</i>	
<i>Leonardo L. Silva, State University of Rio Grande do Sul, Brazil</i>	
<i>Lucas Murliky, State University of Rio Grande do Sul, Brazil</i>	
<i>João Leonardo Fragoso, State University of Rio Grande do Sul, Brazil</i>	
<i>Guilherme Debom, State University of Rio Grande do Sul, Brazil</i>	
<i>Rivalino Matias Jr., Federal University of Uberlandia, Brazil</i>	
<i>Aline Fracalossi, State University of Rio Grande do Sul, Brazil</i>	
<i>Margrit Reni Krug, University of Vale do Rio dos Sinos, Brazil</i>	

<b>Virtual Machine Halt .....</b>	1903
<i>Simão Reis, University of Aveiro, Portugal</i>	
<i>André Zúquete, University of Aveiro, Portugal</i>	
<i>José Vieira, University of Aveiro, Portugal</i>	

## **Practical Aspects of High-Level Parallel Programming Track**

**Track Co-Chairs:** *Frédéric Loulergue, Université d'Orléans, France*

<b>Transforming Javascript Event-Loop into a Pipeline .....</b>	1906
<i>Etienne Brodu, Université de Lyon, France</i>	
<i>Stéphane Frénot, Université de Lyon, France</i>	
<i>Frédéric Oblé, Worldline, France</i>	

<b>Distributed Graph Processing: An Approach based on Overlay Composition .....</b>	1912
<i>Emanuele Carlini, ISTI-CNR, Italy</i>	
<i>Patrizio Dazzi, ISTI-CNR, Italy</i>	
<i>Alessandro Lulli, University of Pisa, Italy</i>	
<i>Laura Ricci, University of Pisa, Italy</i>	

## **Poster Papers**

<b>Sharing Memory in Modern Distributed Applications .....</b>	1918
<i>Claudio Scordino, Evidence Srl, Italy</i>	
<i>Bruno Morelli, Evidence Srl, Italy</i>	

<b>Derivation of Parallel-Efficient Structural Recursive Functions from Declarative Graph Queries .....</b>	1922
<i>Chong Li, National Institute of Informatics, Japan</i>	
<i>Le-Duc Tung, SOKENDAI, Japan</i>	
<i>Xiaodong Meng, Shanghai Jiao Tong University, China</i>	
<i>Zhenjiang Hu, National Institute of Informatics, Japan</i>	

## **Programming Languages Track**

**Track Co-Chairs:** *Marjan Mernik, University of Maribor, Slovenia*  
*Barrett Bryant, University of North Texas, United States*

<b>Track Editorial .....</b>	1926
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<b>Using CSP for Coordinating Undo-Based Collaborative Applications .....</b>	1928
<i>Asma Cherif, Umm al-Qura University, Saudi Arabia</i>	
<i>Abdessamad Imine, Loria University, France</i>	

<b>Integrating Lua into C for Embedding Lua Interpreters in a C Application .....</b>	1936
<i>Akira Tanimura, The University of Electro-communications, Japan</i>	
<i>Hideya Iwasaki, The University of Electro-communications, Japan</i>	
<b>A Performant Scheme Interpreter in asm.js .....</b>	1944
<i>Noah Van Es, Vrije Universiteit Brussel, Belgium</i>	
<i>Jens Nicolay, Vrije Universiteit Brussel, Belgium</i>	
<i>Quentin Stievenart, Vrije Universiteit Brussel, Belgium</i>	
<i>Theo D'Hondt, Vrije Universiteit Brussel, Belgium</i>	
<i>Coen De Roover, Vrije Universiteit Brussel, Belgium</i>	
<b>Stella: A Python-Based Domain-Specific Language for Simulations .....</b>	1952
<i>David Mohr, University of New Mexico, USA</i>	
<i>Darko Stefanovic, University of New Mexico, USA</i>	
<b>A Parsing Machine for Parsing Expression Grammars with Labeled Failures .....</b>	1960
<i>Sérgio Medeiros, Federal University of Rio Grande do Norte, Brazil</i>	
<i>Fabio Mascarenhas, Universidade Federal do Rio de Janeiro, Brazil</i>	
<b>Wodel: A Domain-Specific Language for Model Mutation .....</b>	1968
<i>Pablo Gómez-Abajo, Universidad Autónoma de Madrid, Spain</i>	
<i>Esther Guerra, Universidad Autónoma de Madrid, Spain</i>	
<i>Juan de Lara, Universidad Autónoma de Madrid, Spain</i>	
<b>Integrating Regular Expressions and SNOBOL Patterns into String Scanning: A Unifying Approach .....</b>	1974
<i>Clinton Jeffery, University of Idaho, USA</i>	
<i>Phillip Thomas, National Library of Medicine, USA</i>	
<i>Sudarshan Gaikaiwari, Yelp, USA</i>	
<i>John Goetsche, University of Idaho, USA</i>	
<b>An Embedded Domain Specific Language for Distributed Memory Transactions in Java .....</b>	1980
<i>Jeronimo da Cunha Ramos, Federal University of Pelotas, Brazil</i>	
<i>André Rauber Du Bois, Federal University of Pelotas, Brazil</i>	
<i>Mauricio Lima Pilla, Federal University of Pelotas, Brazil</i>	
<b>An Object Model for a Dynamic Mixin based Language .....</b>	1986
<i>Eden Burton, McMaster University, Canada</i>	
<i>Emil Sekerinski, McMaster University, Canada</i>	

## Poster Papers

<b>Automated Adaptation via Quantitative Partial Model Checking .....</b>	1993
<i>Stefano Bistarelli, Università di Perugia, Italy</i>	
<i>Francesco Santini, Università di Perugia, Italy</i>	
<i>Fabio Martinelli, Institute of Informatics and Telematics of CNR, Italy</i>	
<i>Ilaria Matteucci, Institute of Informatics and Telematics of CNR, Italy</i>	
<b>Principles for Reuse in Formal Language Tools .....</b>	1997
<i>Luis Diogo Couto, Aarhus University, Denmark</i>	
<i>Peter W.V. Tran-Jørgensen, Aarhus University, Denmark</i>	
<i>Kenneth Lausdahl, Aarhus University, Denmark</i>	

<b>Maintainability of Functional Reactive Programs in a Telecom Server Software .....</b>	2001
<i>Klervie Toczé, Ericsson AB, Sweden</i>	
<i>Maria Vasilevskaya, Linköping University, Sweden</i>	
<i>Patrik Sandahl, Ericsson AB, Sweden</i>	
<i>Simin Nadjm-Tehrani, Linköping University, Sweden</i>	
<b>Futex based Locks for C11's Generic Atomics (Extended Abstract) .....</b>	2004
<i>Jens Gustedt, INRIA, France</i>	
<b>A DSL for Executable 'How To' Manuals .....</b>	2007
<i>Marcel Heinz, University of Koblenz-Landau, Germany</i>	
<i>Philipp Helsper, University of Koblenz-Landau, Germany</i>	
<i>Ralf Lämmel, University of Koblenz-Landau, Germany</i>	
<i>Tobias M. Schmidt, University of Koblenz-Landau, Germany</i>	
<b>Extensible Modules for JavaScript .....</b>	2010
<i>Florent Marchand de Kerchove, ASCOLA (Mines Nantes, Inria, LINA), France</i>	
<i>Jacques Noyé, ASCOLA (Mines Nantes, Inria, LINA), France</i>	
<i>Mario Südholt, ASCOLA (Mines Nantes, Inria, LINA), France</i>	
<b>Reliable Software Technologies and Communication Middleware Track</b>	
<b>Track Co-Chairs:</b>	
<i>Marisol García-Valls, Universidad Carlos III de Madrid, Spain</i>	
<i>Aniruddha Gokhale, Vanderbilt University, United States</i>	
<i>Paolo Bellavista, Università di Bologna, Italy</i>	
<b>Track Editorial .....</b>	2013
<b>Scalable Monitoring and Dependable Job Scheduling Support for Multi-Domain Grid Infrastructures .....</b>	2015
<i>Marcello Cinque, Università di Napoli Federico II, Italy</i>	
<i>Antonio Corradi, University of Bologna, Italy</i>	
<i>Luca Foschini, University of Bologna, Italy</i>	
<i>Flavio Frattini, Università di Napoli Federico II, Italy</i>	
<i>Javier Povedano-Molina, Universidad de Granada, Spain</i>	
<b>Exploiting User Feedback for Online Filtering in Event-Based Systems .....</b>	2021
<i>Fabio Petroni, Sapienza University of Rome, Italy</i>	
<i>Leonardo Querzoni, Sapienza University of Rome, Italy</i>	
<i>Roberto Beraldì, Sapienza University of Rome, Italy</i>	
<i>Mario Paolucci, Institute of Cognitive Sciences and Technologies, CNR, Italy</i>	
<b>Poster Papers</b>	
<b>Adjusting Middleware Knobs to Suit CPS Domains .....</b>	2027
<i>Marisol García-Valls, Universidad Carlos III de Madrid, Spain</i>	
<i>Cristian Calva-Urrego, Universidad Carlos III de Madrid, Spain</i>	
<i>Alejandro Alonso, Universidad Politécnica de Madrid, Spain</i>	
<i>Juan A. de la Puente, Universidad Politécnica de Madrid, Spain</i>	

## **Computer Security Track**

**Track Co-Chairs:** Giampaolo Bella, Università di Catania, Italy  
Sergio Maffei, Imperial College London, UK

**Track Editorial** ..... 2031

**Slick: An Intrusion Detection System for Virtualized Storage Devices** ..... 2033

*Andrei Bacs, Vrije Universiteit Amsterdam, The Netherlands  
Cristiano Giuffrida, Vrije Universiteit Amsterdam, The Netherlands  
Bernhard Grill, Technische Universität Wien, Austria  
Herbert Bos, Vrije Universiteit Amsterdam, The Netherlands*

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