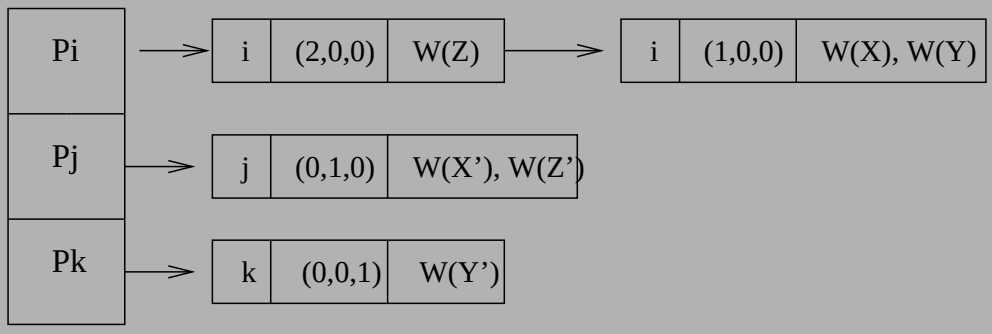
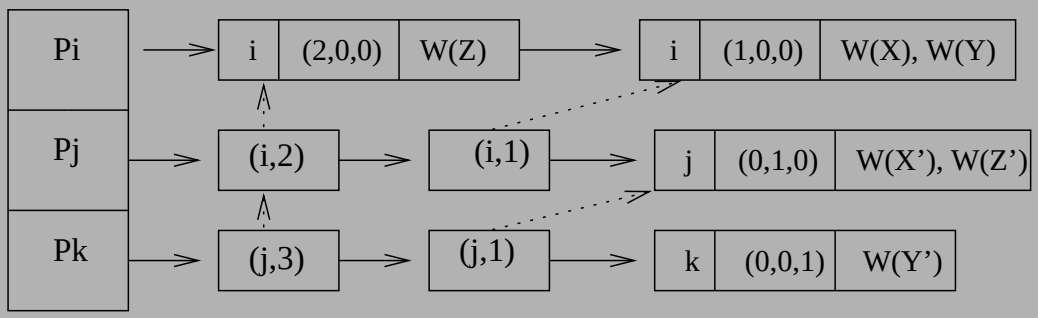


(a) Write Notice Structure Supported by LRC Memory Model



(b) Revised Write Notice Structure